

REMARKS

Claims 1-20 were pending. Claims 6, 12 have been amended. Accordingly, claims 1-20 remain pending subsequent entry of the present amendment.

Information Disclosure Statement

Applicant filed an information disclosure statement via electronic submission April 28, 2004, prior to issuance of the first office action. The confirmation number received by the Applicant for the electronic filing is 2888. For the examiner's convenience, a copy of the previously filed information disclosure statement is attached. Applicant requests the examiner return a signed copy of the statement indicating the references included therein have been considered.

Drawings

In the present Office Action, and objection is made to figures 3A-3C, and figure 4. In particular, the values depicted by item 302 are apparently objected to as not being represented as binary digits (i.e., base 2 representation). However, it is noted that item 302 illustrates, and is described as, a "driving pattern" or "driving sequence" and is not intended to depict, for example, an actual register contents. Rather, the values illustrated by the pattern are symbolic and identify the patterns which are also illustrated in the same figures. For example, as described on page 11, lines 19-20 – "[a]lso illustrated is a sixteen cycle driving sequence 302 depicting which patterns, Pattern 1 ("1") 310 or Pattern 2 ("2") 320, supply a value on a given driving cycle." Applicant believes it is clear the numerals are references which identify the patterns (310, 320). Accordingly, Applicant believes no correction is required. In addition, Applicant believes one skilled in the art would have no difficulty in determining how to represent such values, or other similar identifiers, in a binary system. For example, "01" = "1", and "10" = "2" could be used. Alternatively a single bit could be used to identify one of two patterns.

Claim Objections

Claim 6 has been amended to recite “turn-around”, rather than “turnaround” as requested by the examiner.

35 U.S.C. § 112 Rejections

Claim 12 stands rejected under 35 U.S.C. § 112, second paragraph. Applicant has amended the claim to correct a typographical error and overcome the rejection.

35 U.S.C. § 102 Rejections

In the present Office Action, claims 1-20 stand rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent Application Publication No. 2002/0138802 (hereinafter “Firth”). However, Applicant submits each of the pending claims recite features neither disclosed nor suggested by the cited art. Accordingly, Applicant traverses the above rejections and requests reconsideration.

Claim 1 recites a method which includes

“storing a first value;
storing a second value;
receiving a clock signal;
selecting either said first value or said second value for inclusion in a test pattern
in response to said clock signal;
wherein determining whether to select either said first value or said second value
on a given clock cycle is determined according to a predetermined test
pattern sequence.” (emphasis added).

Applicant submits at least the above highlighted features are not disclosed by the cited art. In the above claim, the values which are selected are for inclusion in a test

pattern. In the Office Action it is suggested that Firth discloses the above highlighted features in the following:

"In the simplest embodiment, the stimulus circuitry stores a single Marinescu test, and the data input to the stimulus scan cells and clocked to the stimulus circuitry acts simply as a commencement signal. However, it is preferred that the stimulus circuitry be capable of applying two or more Marinescu tests to the memory block." (Firth, para. 92).

In contrast to the features recited above, the cited portion of Firth discloses storing and applying Marinescu tests. Marinescu tests are procedures or algorithms which generally describe what a test may comprise. The above disclosure describes a simple embodiment wherein the circuitry stores and applies only a single test. It further suggests the circuitry be capable of applying two or more such tests to the memory block. However, this reference to two or more Marinescu tests is a reference to two distinct tests. In contrast, the above recited features recite selecting individual values for inclusion in a test pattern responsive to the clock signal. Accordingly, claim 1 is believed patentably distinct from the cited art. As each of claims 7 and 13 include features similar to that of claim 1, each of claims 7 and 13 are patentably distinct for similar reasons.

Applicant believes all claims to be patentably distinguished from the cited art for at least the above reason. However, should the examiner believe otherwise, the below signed representative requests and would appreciate a telephone interview to facilitate a more speedy resolution. The representative may be reached at (512) 853-8866.

CONCLUSION

Applicant submits the application is in condition for allowance, and an early notice to that effect is requested.

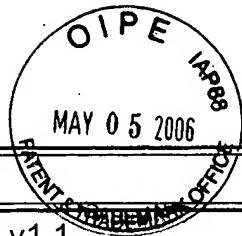
If any fees are due, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert, & Goetzel, P.C. Deposit Account No. 501505/5681-11500/RDR.

Respectfully submitted,

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Date: 5/3/04



TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

COPY

Title of Invention	IBIST TEST FOR SYNCHRONOUS LINES AT MULTIPLE FREQUENCIES
Application Number:	10/689265 *10/689265*
Date:	2003-10-20
First Named Applicant:	BRIAN L. SMITH
Confirmation Number:	2888
Attorney Docket Number:	5681-11500

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Submitted by:	Elec. Sign.	Sign. Capacity
RORY D. RANKIN Registered Number: 47,884	Rory D. Rankin	Attorney

Documents being submitted	Files
us-ids	5681-11500IDS-usidst.xml
	us-ids.dtd
	us-ids.xsl
Comments	
If any required fees are missing, the Commissioner is authorized to charge said fees to Meyertons, Hood, Kivlin, Kowert and Goetzel, P.C. Deposit Account No. 501505/5681--11500/RDR	

UNITED STATES PATENT AND TRADEMARK OFFICE
ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1
 Stylesheet Version v1.1.1



Title of Invention

IBIST TEST FOR SYNCHRONOUS LINES AT MULTIPLE FREQUENCIES

Submission Type: Information Disclosure Statement

Application Number: 10/689265

10/689265

EFS ID: 59927

Server Response:

Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
ICON1	2888
ISYS5	Filename= N/A BusinessRule= Validation System/Function Call Information. #Supporting Msg: Server unable to validate the Confirmation/Application numbers at this time. They will be checked by PTO personnel later.

First Named Applicant: BRIAN SMITH

Attorney Docket Number: 5681-11500

Timestamp: 2004-04-28 09:56:24 EDT

From: us

File Listing:

Doc. Name	File Name	Size (Bytes)
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package-data	package-data.dtd	27025
package-data	us-package-data.xsl	19263
Total files size		69569

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Digital Certificate Holder
 Name: cn=Rory D. Rankin,ou=Registered
 Attorneys,ou=Patent and Trademark
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
Stylesheet Version v18.0

MAY 05 2006

Title of
Invention

IBIST TEST FOR SYNCHRONOUS LINES AT MULTIPLE
FREQUENCIES

Application Number: 10/689265 *10/689265*

Confirmation Number: 2888

First Named Applicant: BRIAN SMITH

Attorney Docket Number: 5681-11500

Art Unit: 2877

Examiner: UNKNOWN UNKNOWN

Search string: (6201829 or 5764655 or 5621741).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6201829	2001-03-13	Schneider			
	2	5764655	1998-06-09	Kirihata, et al.			
	3	5621741	1997-04-15	Kohiyama			

Signature

Examiner Name

Date